

Tech Insights 2020

At-A-Glance Agenda

Day One – October 6, 2020	
9:30am – 10:15am CEST	Semiconductor Industry Trends and IP Perspectives, Joachim Kunkel – Synopsys
	Automotive
11:15am – 12:00pm CEST	Synopsys' Comprehensive Automotive Design Flow for Autonomous Driving and ADAS Applications – Synopsys
12:00pm – 12:20pm CEST	Addressing Functional Safety Issues Early and Cost Effectively! – Synopsys
12:20pm – 12:45pm CEST	GF® 22FDX® AG1 Automotive Design Platform – GLOBALFOUNDRIES
	Analog Custom Design
11:15am – 12:15pm CEST	Reimagining Analog Design and Layout—Why Custom Compiler is Proven to be a Better Alternative – Synopsys
12:15pm – 12:35pm CEST	Design of CMOS Image Sensors with Custom Compiler – IMASENIC
	Long Reach Performance and Reliable Die-to-Die Links
11:15am – 12:15pm CEST	Ensuring True Long Reach Performance and Reliable Die-to-Die Links in 800G Hyperscale Data Center SoCs – Synopsys
Day Two – October 7, 2020	
	Virtual Prototyping for Automotive Software Development
9:30am – 10:00am CEST	Software Development using Next Generation Infineon AURIX™ Virtual Prototypes – Infineon Technologies AG
10:00am – 10:30am CEST	Scaling Functional Safety Testing of Electronic Power Steering Systems with Virtualizer – Volkswagen AG
10:30am – 11:00am CEST	From System to Software: A Study in Efficient, Robust Design for Electric Vehicles – Synopsys
	Robust Analog Mixed Signal Verification – Part 1
9:30am – 10:30am CEST	Delivering Safe and Reliable AMS Designs with Synopsys TestMAX CustomFault – Synopsys
10:30am – 10:45am CEST	Analog Fault Simulation in ISO26262 Applications – TDK-Micronas GmbH
10:45am – 11:00am CEST	TestMax CustomFault for Failure Analysis of Analog Mixed Signal IP's – STMicroelectronics
	Automotive IP for ADAS Applications
9:30am – 10:30am CEST	Automotive IP Supporting ASIL D Systematic for ADAS Applications – Synopsys
	Synopsys Verification Continuum Platform
11:15am – 12:45pm CEST	Synopsys Verification Continuum—Finding the Tough Bugs in your Hardware and Software – Synopsys
	Robust Analog Mixed Signal Verification – Part 2
11:15am – 12:15pm CEST	Mixed Signal Verification—How Designers use Synopsys's Mixed-Signal Solution to Develop Intelligent Testbenches, Improve Simulation Performance and Analog Test Coverage – Synopsys
12:15am – 12:35pm CEST	eSTM Flash Memory IP AMS Verification Strategy & Device Modelling Improvements – STMicroelectronic
	High-Performance Real-Time Processors for Embedded Applications
11:15am – 12:15pm CEST	How to Get from Here to There in Real Time: The Future of High Performance Embedded Processing – Synopsys